

Fig. 1A

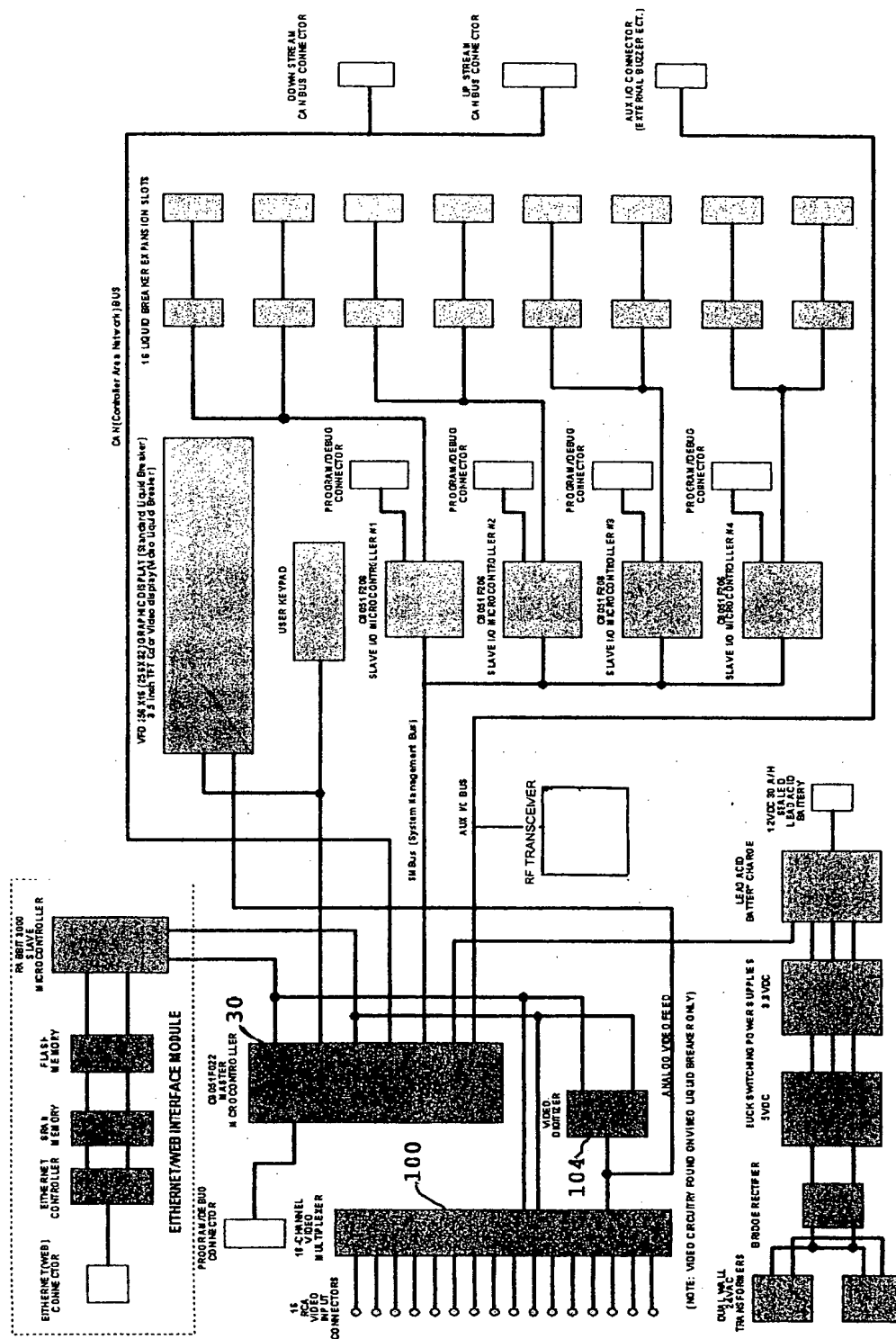


FIG. 1B

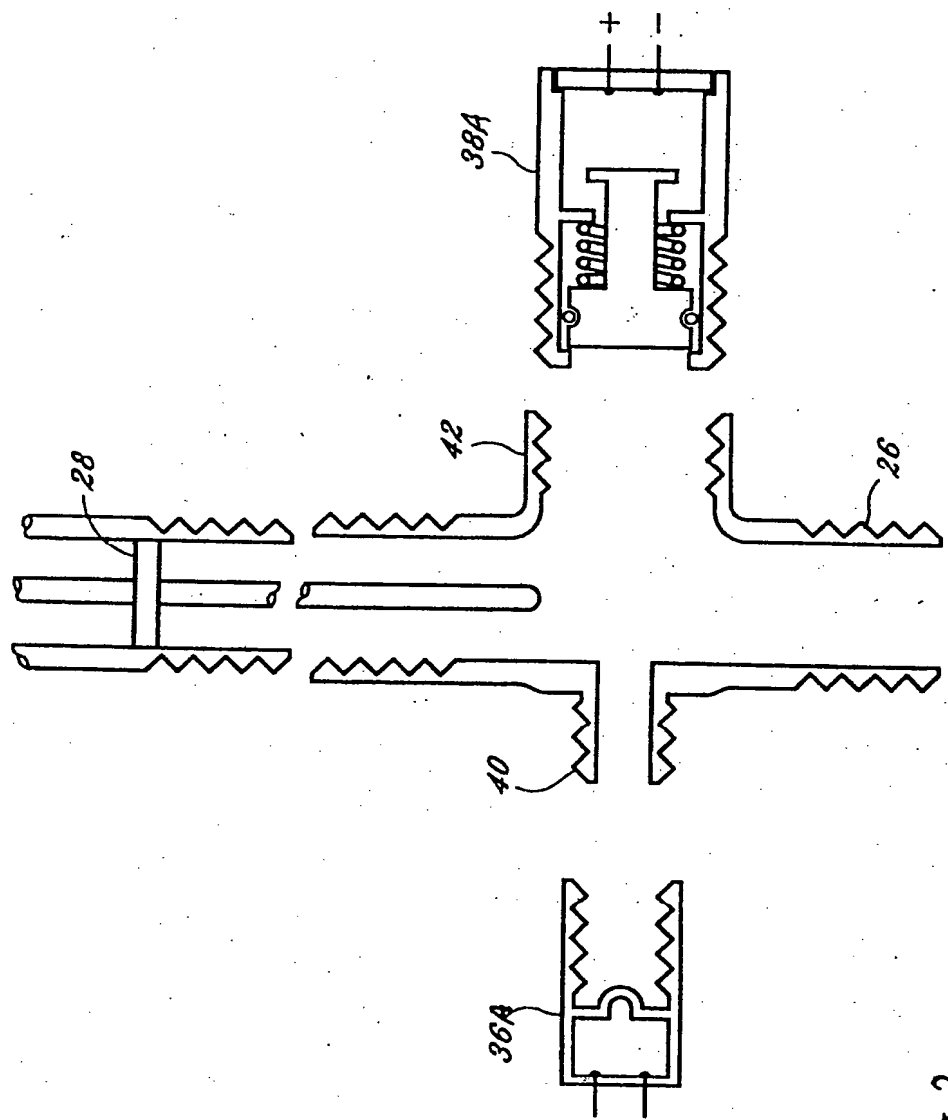


Fig. 2

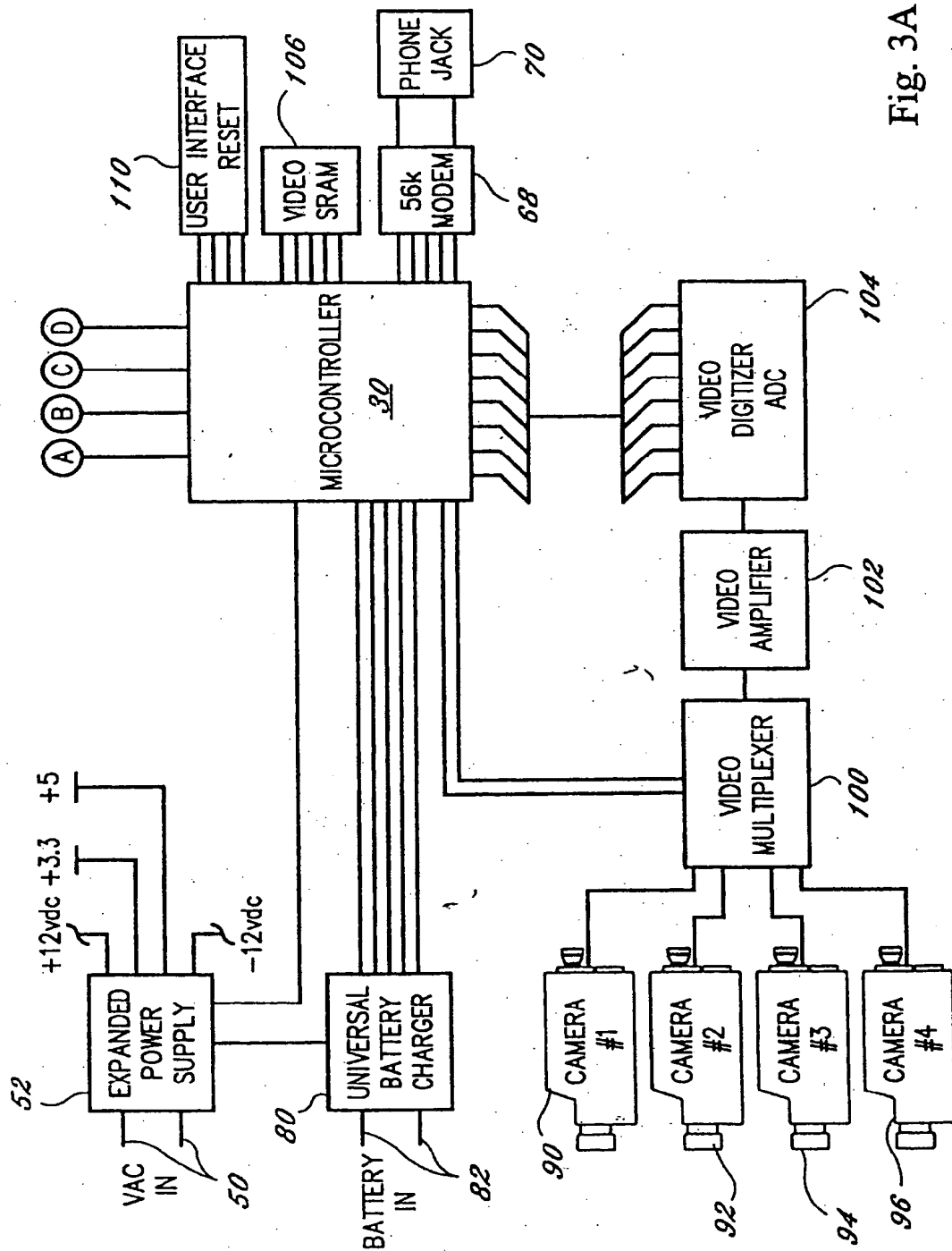


Fig. 3A

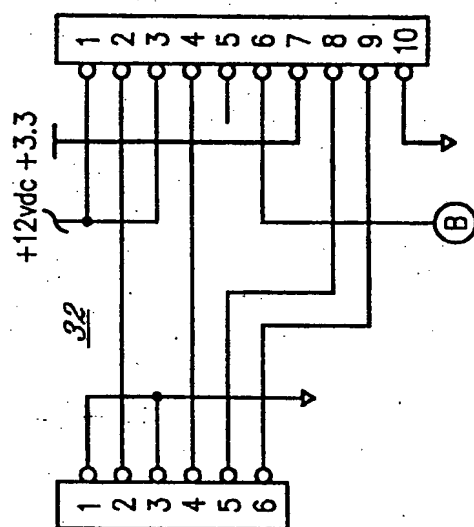
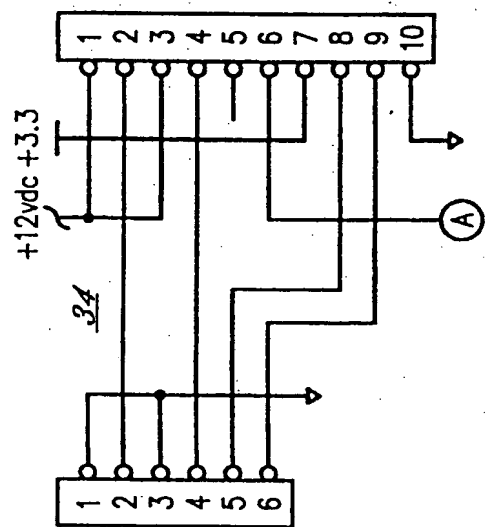
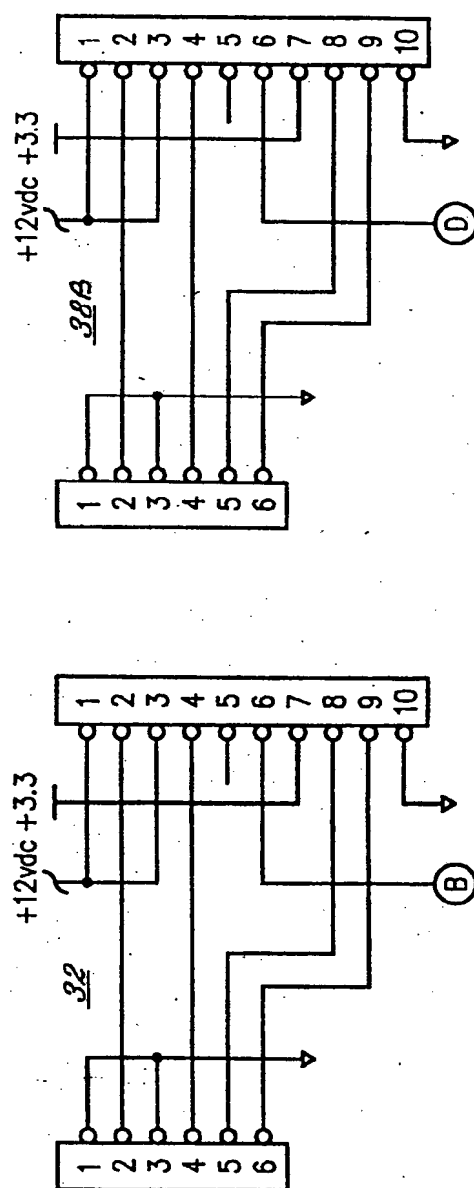
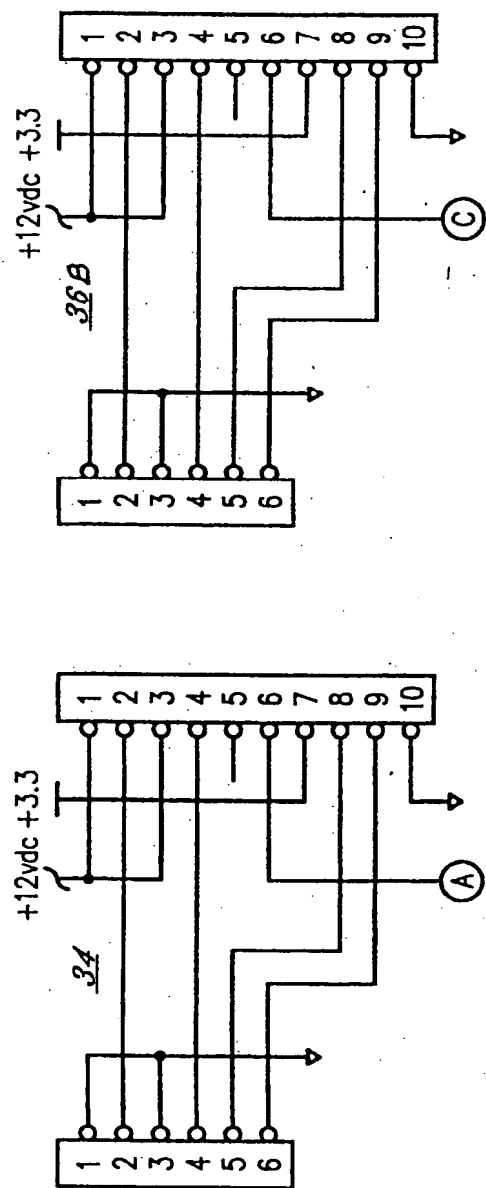


Fig. 3B

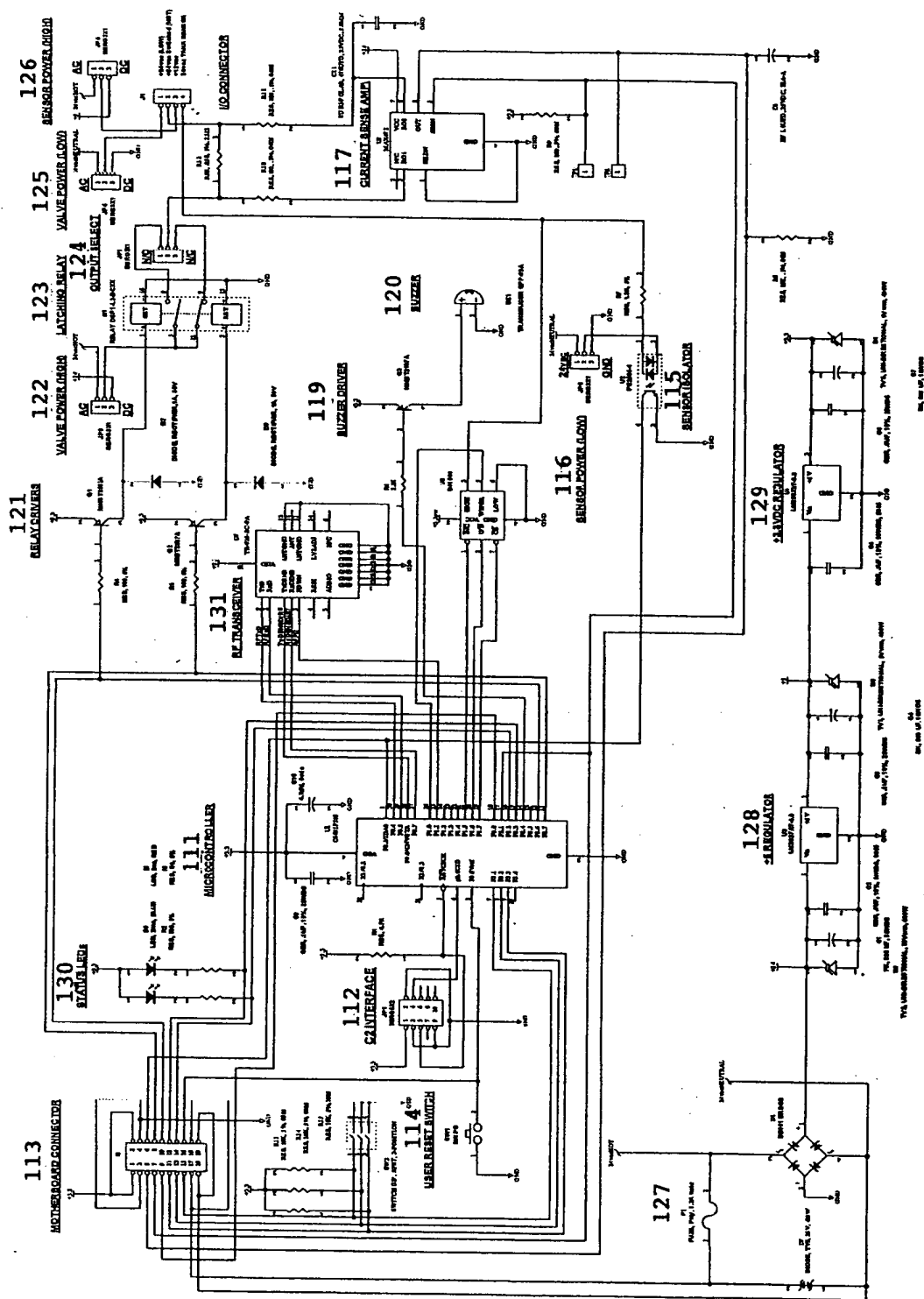
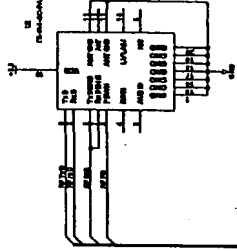


FIG. 4

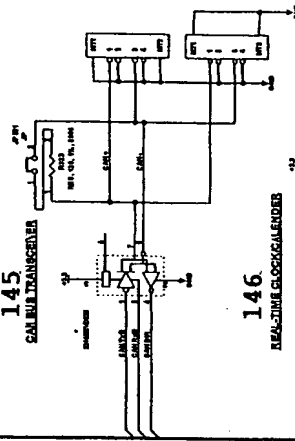
144

RF TRANSDUCER



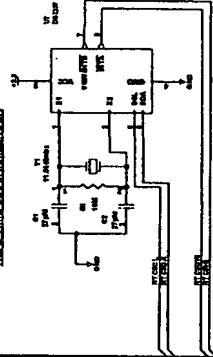
145

GAN RUN TRANSFORMER



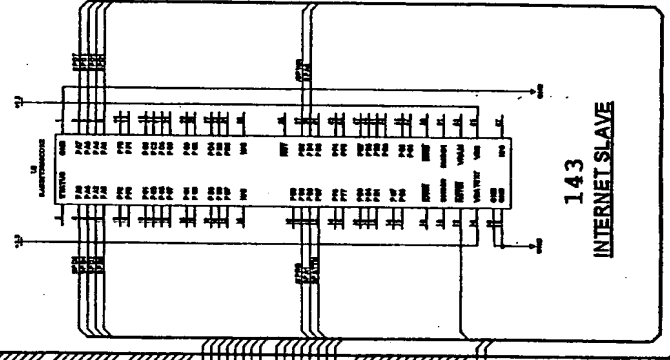
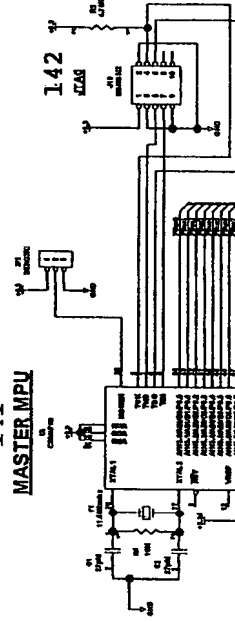
146

REAL-TIME CLOCK/CALENDAR



141

MASTER MPU



143

INTERNET SLAVE

FIG. 5

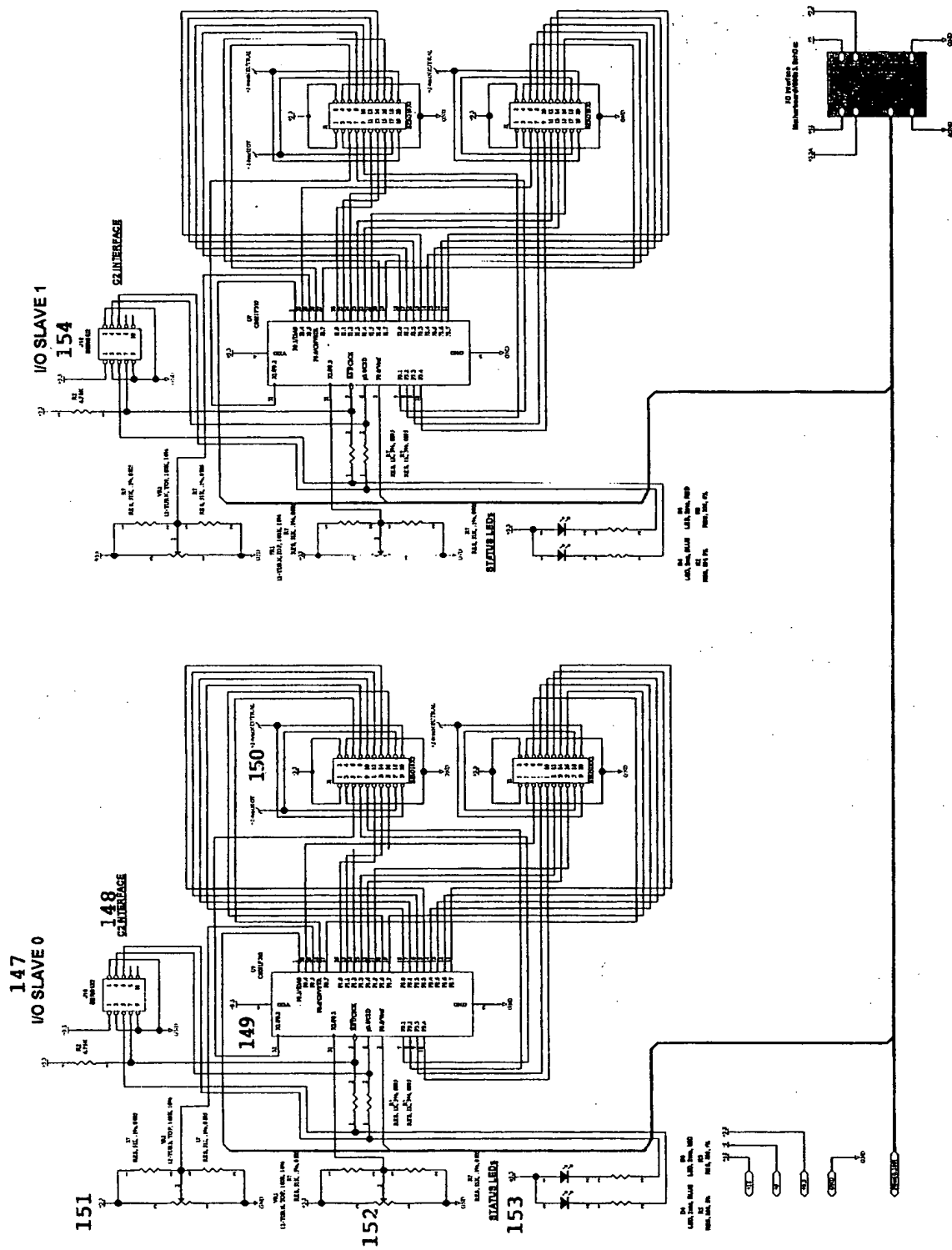
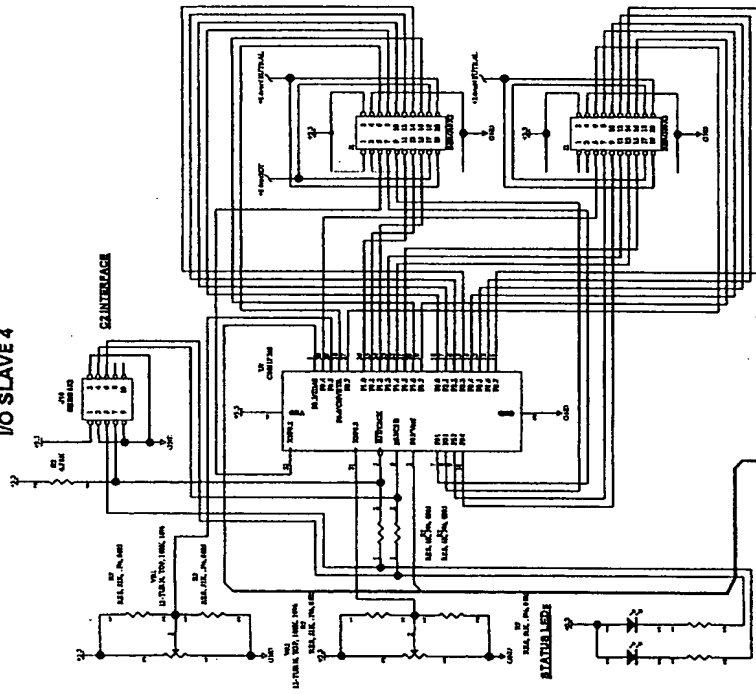


FIG. 6A

157
I/O SLAVE 4



158
I/O SLAVE 6

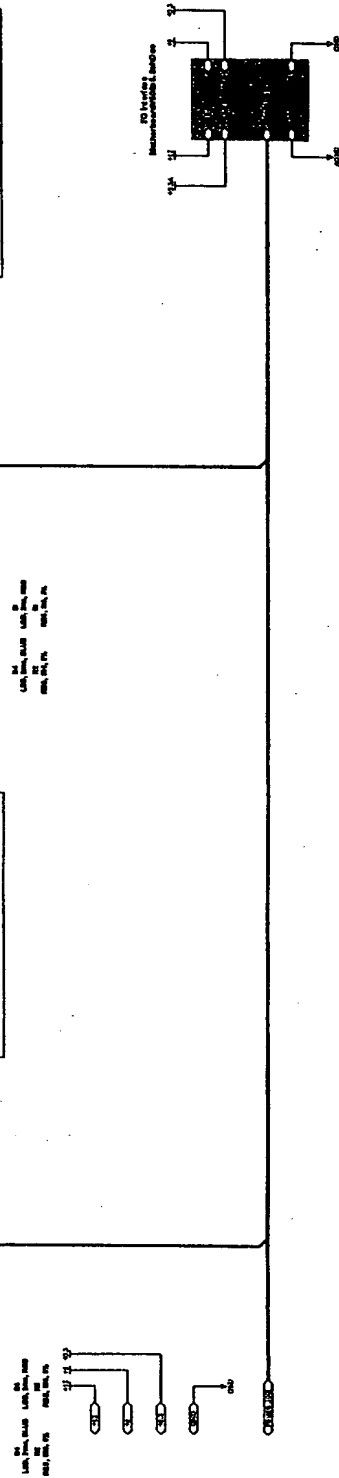
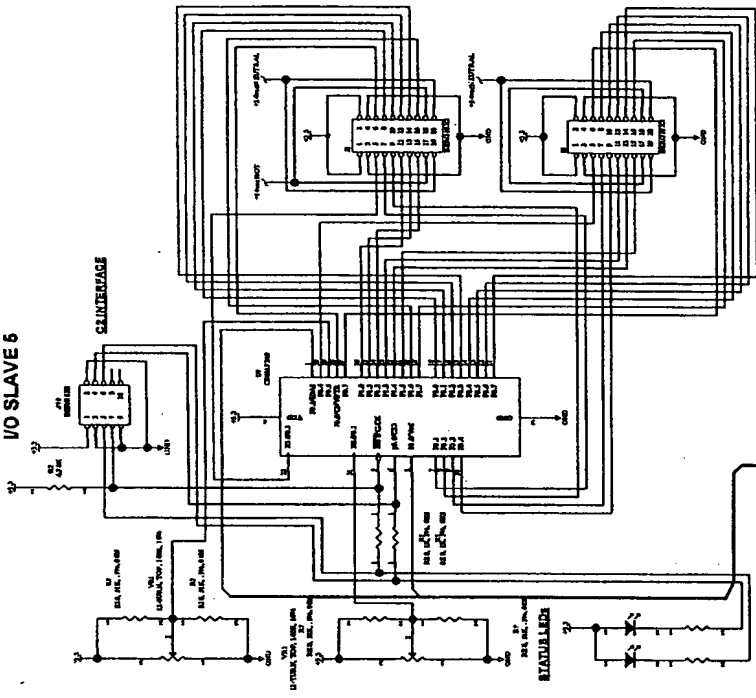
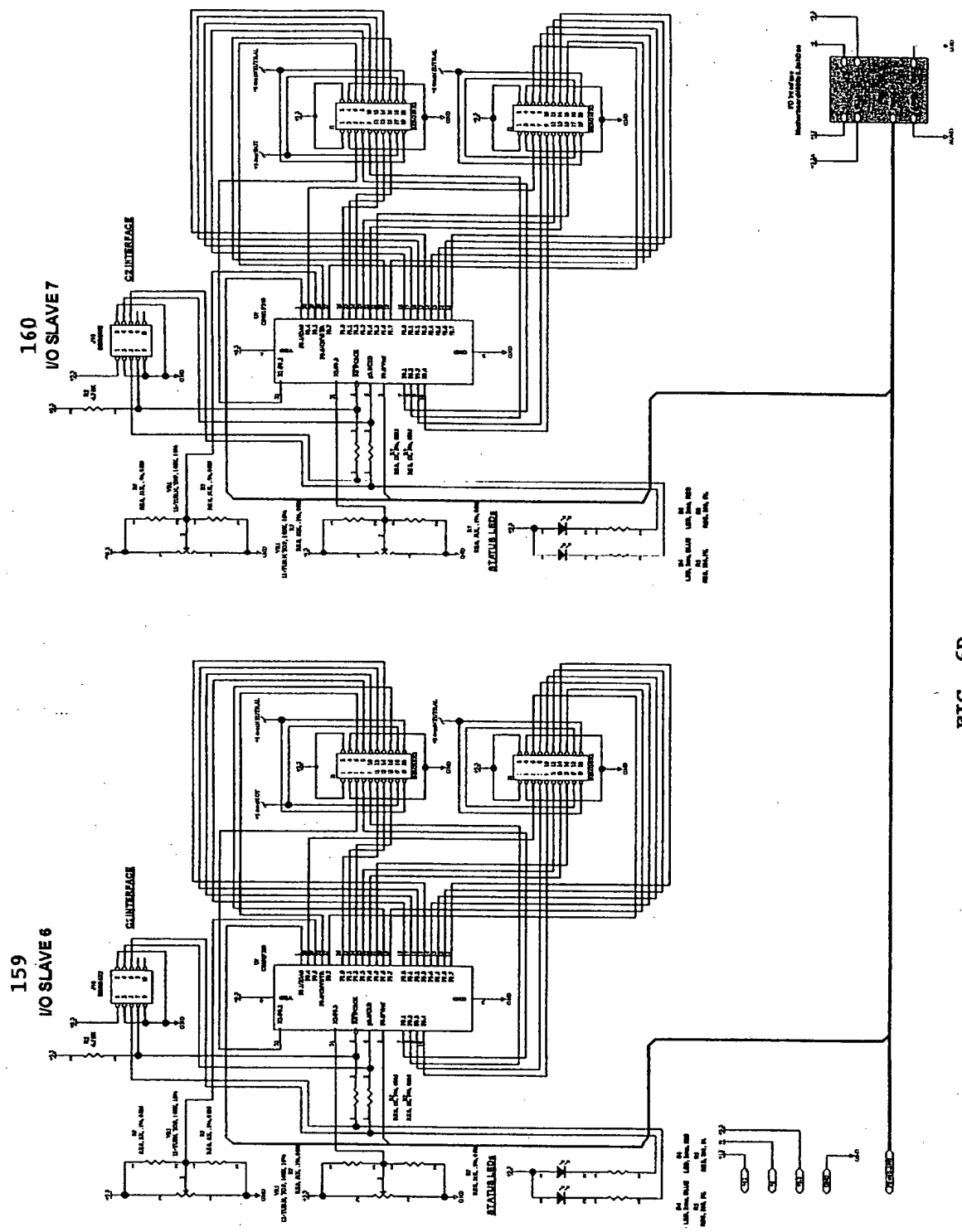


FIG. 6C



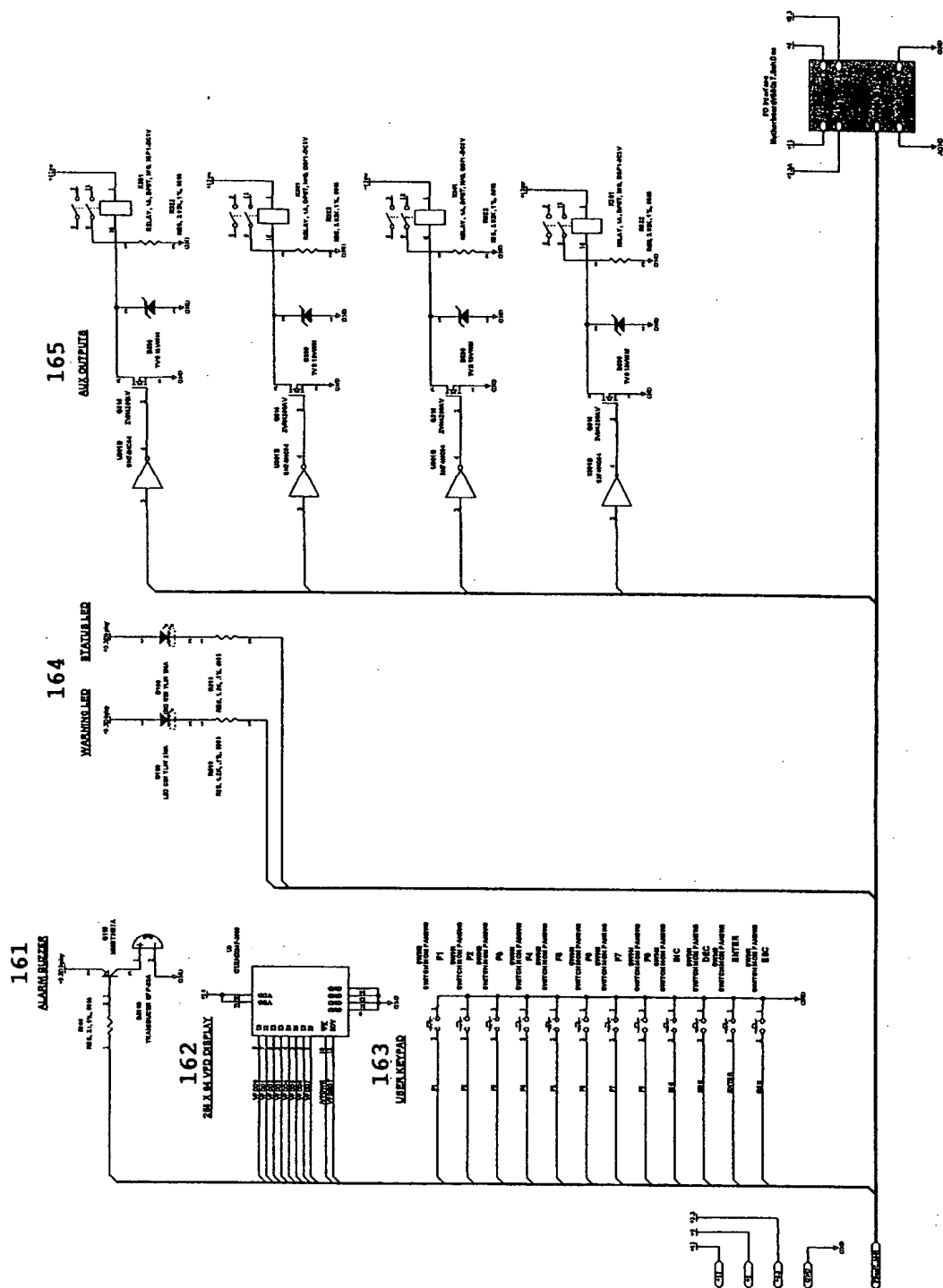
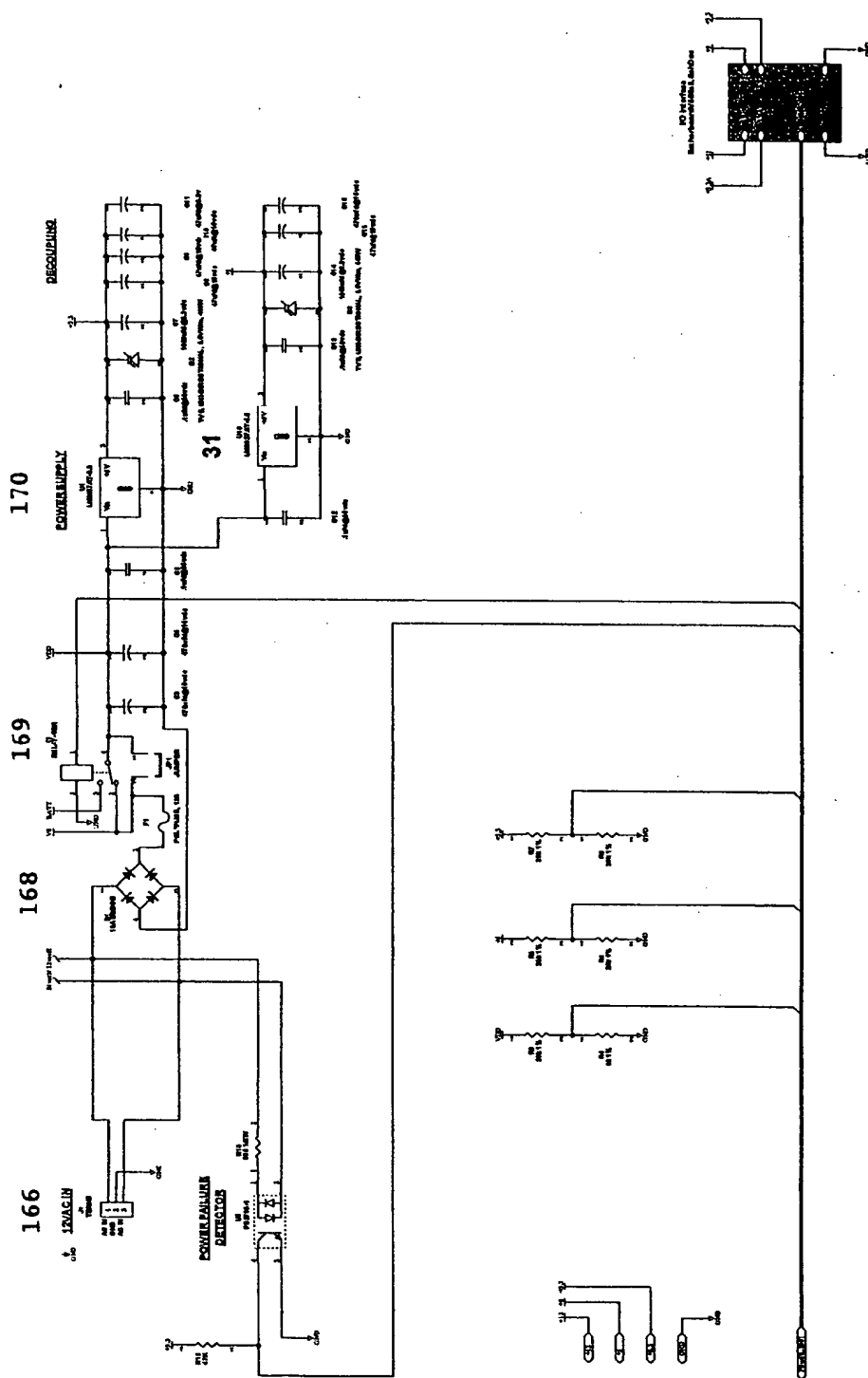


FIG. 7



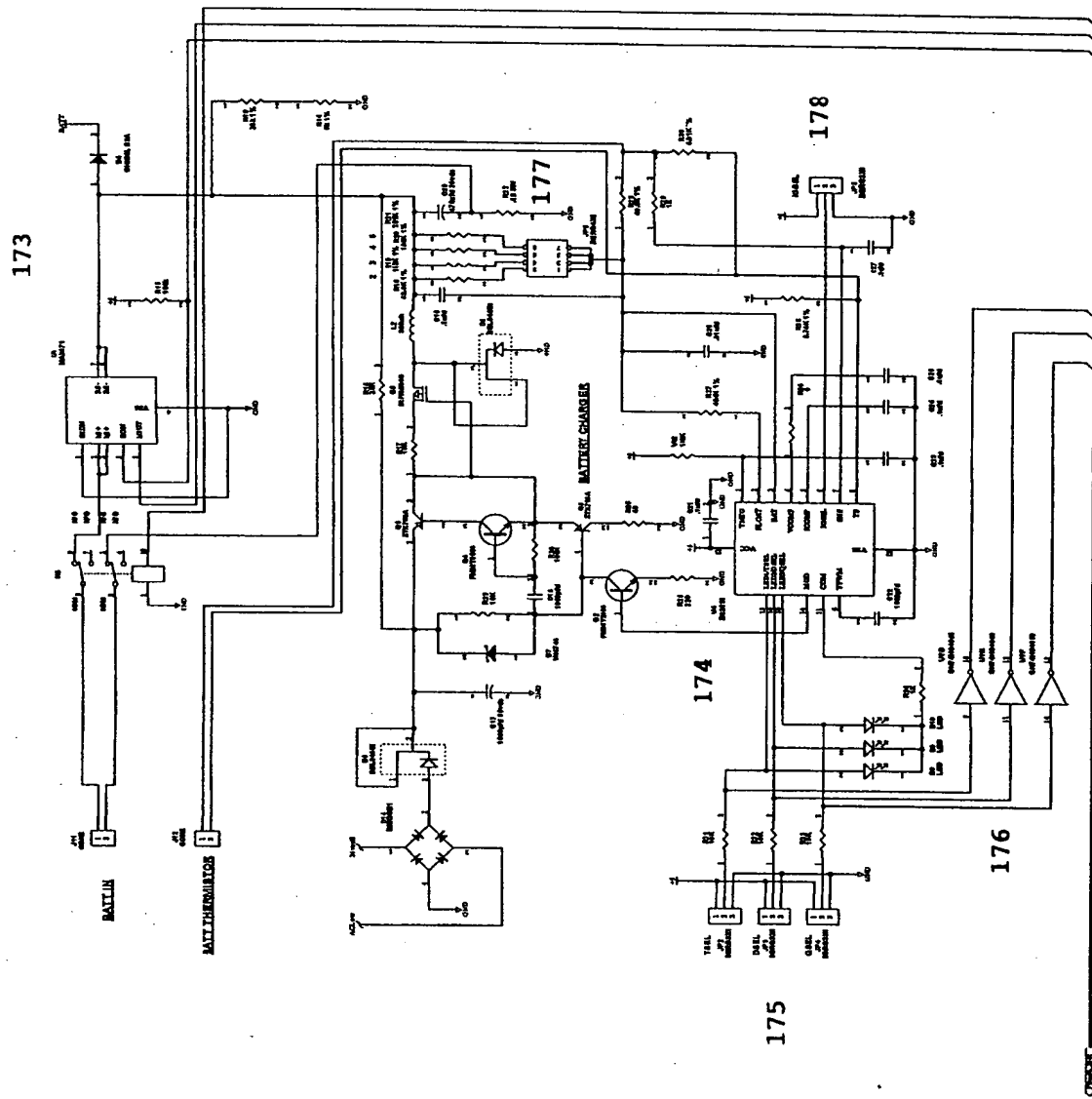


FIG. 9

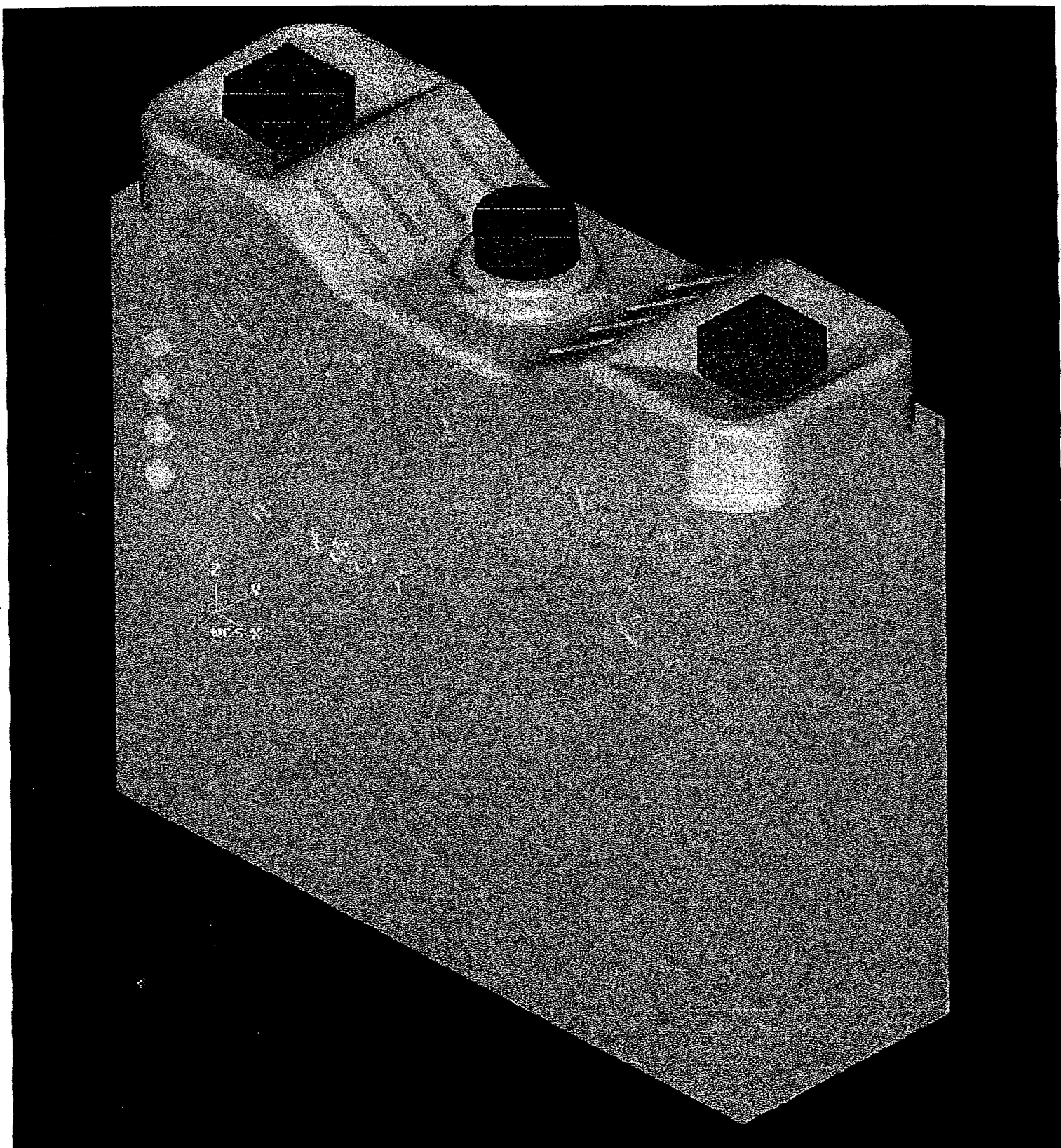


FIG. 10

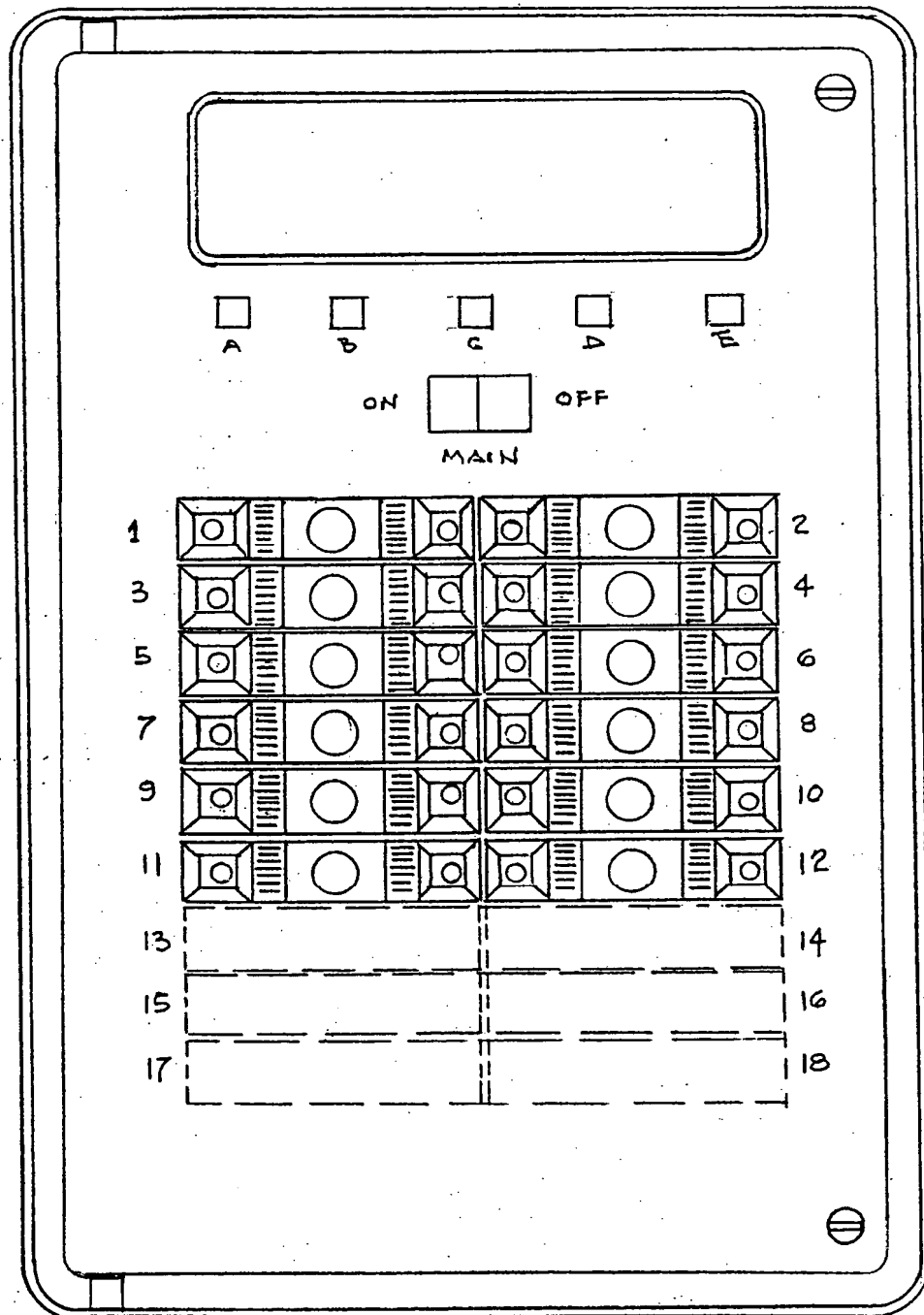


FIG. 11